Dragonchip

User Manual for Gang-Socket board ver1.0

Document Revision 1.0 May, 2005

Revision History

The following table shows the revision history for this document.

Date	Document Revision	Revision
May, 2005	1.0	Preliminary for Gang Socket Board v1.0

Contents

1 Introduction

The Objective of this document is to provide the user a quick start to use gang socket board for single line programmer.

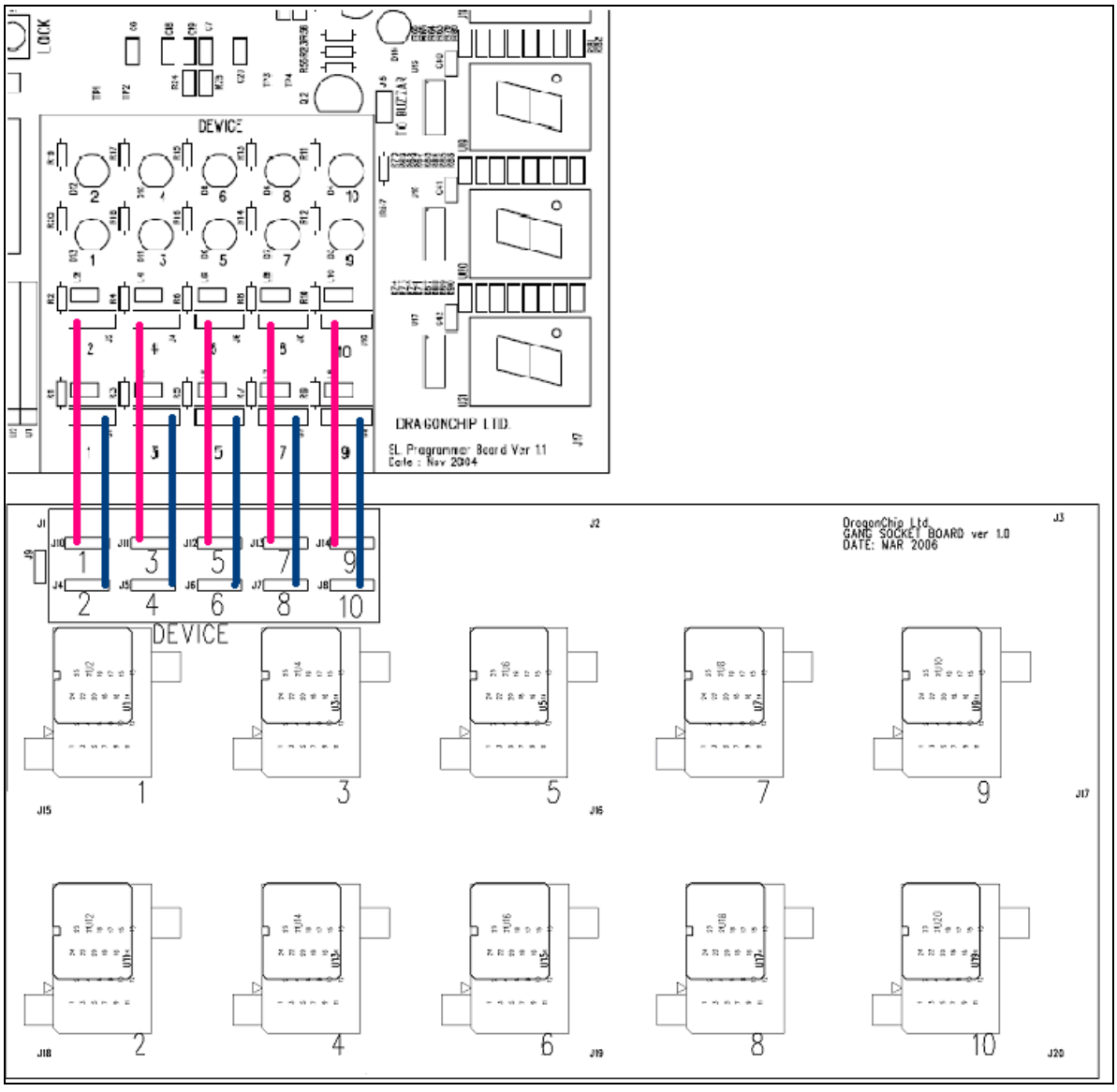
Gang Socket board acts as an expansion module for single line programmer. It can program up to 10 chips for each programming cycle. It provides more convenience programming method for DRAGONCHIP DC6688 families.

This board is applicable to:
1) DC6688FS Family
2) DC6688FSA Family
3) DC6688FE Family
4) DC6688FL32

This socket board cannot work standalone. It should be work together with the single line programmer.

2 Cable connection between SL Programmer Board and GangSocket board

The Gang Socket board package is included ten cables. These cables are use to connect the Gang Socket board to single line programmer. The suggested connection is as shown in the following:

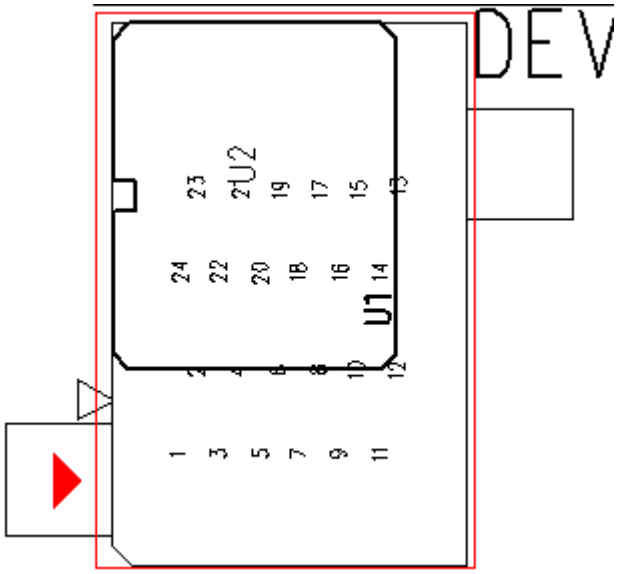


Important note:
Please complete the cables connection before power up the single line programmer. It is not suggested to remove or connect the cables during operation.

3 Device Placement and Jumper Settings

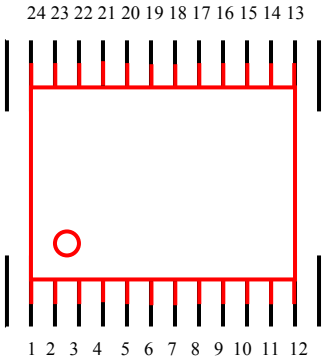
3.1 Device Placement for 6688 Families

Place the device in the right position in the burn-in socket as shown below.

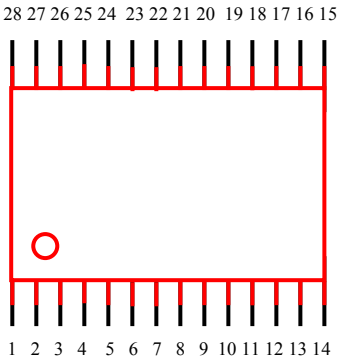


Top view

The red triangle as shown above indicates the orientation of the chip placement.



DC6688FS/DC6688FSA placement



DC6688FE/DC6688FL32 placement

3.2 Jumper Settings for system clock selection while single line programming

The system clock can be provided by either on-board crystal or external clock source. The corresponding jumper setting is as shown in the following:

J21~J30	Clock Source to the chip
1 – 2 short	External clock source
2 – 3 short 4 – 5 short	On board crystal 12MHz.(Default) SL programmer board [1] should choose 12MHz to do programming for synchronization.

Remarks:
[1] Details on how to select frequency on SL programmer board ver1.1, refers to document “SL Programmer Board ver1.1 Manual” - section “Hardware Settings”.

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